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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	09/751747	Docket Number	MIPS.0105-00-US
Filed	12/29/00	Group Art Unit	2181
Examiner	WILLIAM M. TREAT	Customer No.	23669
Application Title	CONFIGURABLE OUT-OF-ORDER DATA TRANSFER IN A COPROCESSOR INTERFACE		
First Named Inventor	LAWRENCE H. HUDEPOHL		

FACSIMILE COVER SHEET

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				Application Number	09/751747
				Filing Date	12/29/00
				First Named Inventor	Lawrence H. Hudepohl
				Group Art Unit	2181
				Examiner Name	William M. Treat
				Attorney Docket Number	MIPS.0105-00-US
Sheet	1	of	1		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	FALOUTSOS, CHRISTOS et al., "Description and Performance Analysis of Signature File Methods for Office Filing." ACM Transactions on Office Information Systems. July 1987. pp237-257	
	AB	Embedded Trace Macrocell (Rev 1) Specification, (2000)	
	AC	EGGERS et al. "The Effect of Sharing on the Cache and Bus Performance of Parallel Programs." ACM, pp.257-270, 1989	
	AD	PEIR et al. "Improving Cache Performance with Balanced Tag and Data Paths." ACM ASPLOS VII, pp268-278. 1996	
	AE	PANDA et al. "Data and Memory Optimization Techniques for Embedded Systems." ACM TDAES, vol 6, no. 2, pp.149-206. 4/2001	
	AF	Embedded Trace Macrocell Specification, Rev. 0/0a, ARM IHI 0014C, ARM Ltd. (1999)	
	AG	EGGERS, SUSAN J. et al. "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor." University of Washington. 1990 ACM, pp. 37-47.	
	AH	UHLIG, RICHARD A. et al. "Trace-Driven Memory Simulation: A Survey." Intel Microcomputer Research Lab; University of Michigan, ACM Computing Surveys. Vol. 29, No. 2, June 1997. pp. 128-170.	
	AI	ROTENBERG, ERIC et al. "Trace Processors." University of Wisconsin. 1997 IEEE Service Center. 12 pp.	
	AJ	ELNOZAHY, E.N. "Address Trace Compression Through Loop Detection and Reduction." 1999 ACM 1-58113-083, pp. 214, 215.	
	AK	JONES, DARREN. MIPS64™ 5Kc™ Processor Cores User's Manual. Rev. 1.0, July 4, 1999. pp. 6-1 to 6-26.	
	AL	CHOW, F. et al. "Engineering a RISC Compiler System." IEEE COMP-CON. March 1986. pp. 132-137.	
	AM	SMITH, M.D. "Tracing with Pixie." Technical Report CSL-TR-91-497. Stanford University, Computer Systems Laboratory, November 1991. pp.1-29	
	AN	ATOM Reference Manual. Digital Equipment Corporation. Massachusetts, December 1993. pp.1-32	
	AO	SRIVASTAVA, A. et al. "ATOM: A System for Building Customized Program Analysis Tools." WRL, Research Report 94/2, Digital Equipment Corporation. March 1994, pp.1-23	
	AP	ATOM User Manual, Digital Equipment Corporation, March 1994. pp. 1-28	
	AQ	CMELIK, B. et al. "Shade: A Fast Instruction-Set Simulator for Execution Profiling." Proceedings of the 1994 ACM SIGMETRICS Conference. SIGMETRICS, California. May 1994. pp. 128-137.	
	AR	MIPS64 5Kc™ Processor Core Software User's Manual. Revision 2.2 August 11, 2000. Document Number: MD00055.	
	AS	MIPS64 5Kc™ Processor Core Datasheet. Revision 1.7.5. August 11, 2000. Document Number MD00014	
	AT	MIPS64 5Kc™ Processor Core Datasheet. Revision 2.0. August 28, 2000. Document Number MD00014	
	AU	MIPS64™ 5Kf™ Processor Core Datasheet. Revision 00.11. March 30, 2001. Document Number: MD00109.	
	AV	MIPS64 5K™ Processor Core Family Integrator's Guide. Revision 02.00 January 15, 2001. Document Number: MD00106.	
	AW	MIPS64 5Kc™ Processor Core Integrator's Guide. Revision 1.2 August 11, 2000.. Document Number: MD00056.	
	AX	MIPS Technologies. "Core Coprocessor Interface Specification." Revision 1.11 March 30, 2001 Document Number: MD00068	
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